

Appl. No. 10/605,837  
Amdt. dated March 30, 2006  
Reply to Office action of February 01, 2006

**Amendments to the Claims:**

1. (currently amended) A method for enabling a computer to self-start comprising:  
selecting a predetermined time for self-start when the computer is on;  
adjusting an alarm setting stored in a memory of an RTC/NVRAM chip (Real-time  
5 Clock/Non-Volatile RAM memory chip) according to the predetermined time;  
powering the computer off; and  
providing electrical power with a power supply if a clock value of the RTC/NVRAM  
chip matches the alarm setting by:  
sending a power on signal to the power supply via a power supply  
10 connector on a motherboard of the computer;  
powering voltages of pins of the power supply connector to appropriate  
levels;  
checking if the voltages in the pins of the power supply connector are  
stable;  
15 sending a power good signal from the power supply to a processor of the  
computer; and  
starting the computer upon receiving the power good signal.
2. (original) The method of claim 1 further comprising:  
20 enabling the System Control Interrupt (SCI) bit in a Southbridge chipset of the  
computer; wherein the Southbridge chipset is able to respond to a matching  
signal sent from the RTC/NVRAM chip when the computer is off.
3. (original) The method of claim 2 further comprising:  
25 employing a BIOS to enable the SCI bit in the Southbridge chipset.
4. (original) The method of claim 1 further comprising:  
sending a matching signal from the RTC/NVRAM chip by changing the value of the

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11<sup>th</sup> byte in the memory of the RTC/NVRAM chip;

- 5           5. (original) The method of claim 1 further comprising:  
            activating the SCl pin of a Southbridge chipset in the computer to send a power on  
            signal in response to a match between the clock value of the RTC/NVRAM chip  
            and the alarm setting stored in the memory of the RTC/NVRAM chip.
6. (cancelled)
- 10       7. (original) The method in claim 1 wherein selecting the predetermined time further  
            comprises:  
            using an application of an operating system in the computer to select the  
            predetermined time.
- 15       8. (original) The method in claim 7 wherein the application of an operating system  
            employs a driver to relay the selected predetermined time to the BIOS.
- 20       9. (original) The method in claim 1 wherein the adjusting of the alarm setting further  
            comprises:  
            employing a BIOS to adjust the alarm setting in the memory of the RTC/NVRAM  
            chip.

Claims 10-16 (canceled)